



Docket No.: M4065.0468/P468
(PATENT)

#12
9.17.03
ICM

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Phillip E. Byrd, et al.

Application No.: 09/941,761

Allowed: May 20, 2003

Filed: August 30, 2001

Group Art Unit: 2812

For: STRUCTURE FOR TEMPORARILY
ISOLATING A DIE FROM A
COMMON CONDUCTOR TO
FACILITATE WAFER LEVEL
TESTING (AS AMENDED)

Examiner: A. Stevenson

COMMENTS ON EXAMINER'S STATEMENT
OF REASONS FOR ALLOWANCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicant has reviewed the Examiner's Reasons for Allowance, mailed on May 20, 2003 (part of Paper No. 11). The Examiner provided reasons for allowance with respect to claims 1 and 42.

Applicant note that the allowed claims define over the prior art for many different reasons. The reasons stated in the Examiner's Statement of Reasons for Allowance should be interpreted as highlighting only one of the many differences between prior art and the claimed invention.

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Otherwise, Applicant agrees with the Examiner that the claimed invention is not described or suggested in the prior art.

Dated: August 20, 2003

Respectfully submitted,

By 

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PART B - FEE(S) TRANSMITTAL

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/941,761	08/30/2001	Phillip E. Byrd	M4065.0468/P468	7373

TITLE OF INVENTION: METHOD AND STRUCTURE FOR TEMPORARILY ISOLATING A DIE FROM A COMMON CONDUCTOR TO FACILITATE WAFER LEVEL TESTING

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1300	\$300	\$1600	08/20/2003

EXAMINER	ART UNIT	CLASS-SUBCLASS
STEVENSON, ANDRE C	2812	438-347000

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Micron Technology, Inc.

Boise, ID

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(Date)

Thomas J. D'Amico Reg. No. 28,371 8/20/03

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